

#### NARSIMHA REDDY ENGINEERING COLLEGE

Maisammaguda, Dhulapally, Kompally, On Medchal Road Secunderabad -500 100.

(Approved by AICTE, New Delhi, Affiliated to JNTU, Hyderabad)

ACCREDITED BY NBA, NAAC WITH A-GRADE

UGC 2(f) & 12(B) An ISO 9001:2015 Certified Institute



#### **DEPARTMENT OF EEE**

#### **Publication of Papers in Journals:**

Table 5.7.1.1 Number of quality publications in Academic Year 2018-19

S No	D a t	Name	Title of the Paper	Details of the publications
1	29 <sup>th</sup> Apri 1- 2019		Estimation of nonlinear hybrid systems using 2 <sup>nd</sup> order Q- Adaptive central Difference Kalman filter	AJCT( accepted) on 29 <sup>th</sup> April-2019
	April- 2019	Dr. Sayanti chatterjee	Intend of hybrid circuit in wind power generation with DFIG for elevated power quality	IJITR vol-7, April-2019
2	2018		An interleaved SEPIC converter connected multilevel inverter using fed isolated synchronous rectifier in plug in battery charge system	IEEE transaction on industrial electronics
	2018	Dr. K. Eswaramoorthy	Control of three phase four wire asymmetrical fifteen level inverter using hybrid Bi- Tri dimensional space vector pulse width modulation.	Journal of electrical engineering vol-18 N0.4, 2018
3	2019	Dr. M Ayyakrishnan	Improvement of Reliability performance under the effect of shunt compensation in power system Inter connected Network	Journal of Advanced Research in Dynamic and Control Systems (scopus) accepted-June 2019
4	2018		Wind power generation with transformer less operation with CSI in offshore condition	IJRECE VOL. 6 ISSUE 4 ( OCTOBER- DECEMBER 2018) ISSN: 2348-2281 (ONLINE)

2018	L.Phani Kumar	Intend and Accomplishment	IJRECE VOL. 6 ISSUE 4
		of Flyback Converter for	(OCTOBER- DECEMBER
		Diverse Type of Voltage	2018) ISSN: 2393-9028
		Loads with High Efficiency	(PRINT)   ISSN: 2348-2281
		· ·	(ONLINE)
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## Number of quality publications in Academic Year 2017-2018

 Table 5.7.1.2 Number of quality publications in Academic Year 2017-2018

S No	Date	Name	Title of the Paper	Details of the publications
1	16/03/201 8 & 17/03/201 8	K. S. Deva Rani	Mitigation of Power Quality disturbances by using different wavelet Transforms	ICPEDES(conference)
	2018		Increase the efficiency in wind turbine system by using DFIG	IJITRISSN: 2320 –5547Volume No.5, Issue No.5, pages:7298-7300.
2	16/03/2018 & 17/03/2018	D. SreeValli	Mitigation of Power Quality disturbances by using different wavelet Transforms	ICPEDES(conference)
3	JANUARY 2018		An enhanced power sharing scheme for voltage unbalance and harmonic compensation in an islanded ac Microgrid	IJIEMR Volume 07, Issue 01, Page No: 204 – 207. ISSN 2456 – 5083
3	October - November 2017	L.Phani Kumar	Practical Model On A Hybrid Series Active Power Compensator For Increase The Power Quality	(IJITR) International Journal OF Innovative Technology and RESEARCH Volume No.5, Issue No.6, October - November 2017, 7539-7542.
4	2017	B. Chandraiah	Design and Implementation of Multi- Input Transformer- Coupled Bidirectional DC-DC Converter with ANN Circuit	IJITR ISSN:2320 –5547 Volume 5 Issue No.6, Pages: 7546-7548.
5	2017	Y. Narasimha Rao	Integration of Solar And PV Battery With Advanced Control Strategy of a Three-Level NPC Inverter	IJRAET Volume 6 Issue No.2, Pages: 36-43

	A High efficiency	IJATES ISSN: 2348-7550
	resonant converter for	Volume 5 Issue No.4,
	wind power generation	Dagger 207 204
	under rapidly changing	Pages: 387-394
	environmental	
	conditions	

## Number of quality publications in Academic Year 2016-2017

Table 5.7.1.3 Number of quality publications in Academic Year 2016-2017

S No	Date	Name	Title of the Paper	Details of the publications
1	2016	Dr.N.Srinvasa Rao	Development Of Circuit For Standalone Solar PV Hybrid System	IJITR ISSN: 2320 –5547 Volume No.6, Issue No.1, Pages: 7825-7827
			Intend And Functioning Of Dvr For Eradication Of Voltage Sag And VoltageSwell Exertions	IJPAM (scoups) ISSN: 1311-8080 (printed version); ISSN: 1314- 3395 (on-line version) Volume 118 No. 14, Pages: 661-665
			A New Multifunctional Dvr For Compensation Of Voltage Sag	IJET (scoups) Volume 7(2.20) Pages: 389-393
2	2017	B. Naresh	A grid tied SPV system with adaptive DC link voltage for CPI voltage variations using fuzzy logic control	IJITR ISSN: 2320 –5547 Volume No.5, Issue No.5, pages:7294- 7297.
	2017		Energy endorsement system increased for analyze in effort and component benefit in micro grid	IJITR ISSN: 2320 –5547 Volume No.6, Issue No.1, Pages:7819- 7821.
	2016		To eradicate the harmonics at Microgrid by using dual interfacing Circuit	IJITR ISSN: 2320 –5547 Volume No.5, Issue No.6 Pages:7593 -7595
3	2016	G. Swetha Bindu	High Effectiveness Wind Power Generation By Using DFIG With SSFCL	IJITR ISSN: 2320 –5547 Volume No.5, Issue No.6, pages: 7588- 7590.

# Papers published / Presented by PG students:

Name of the Student and Class	Associated Faculty member	Presentation Details
D. Avinash & M.Tech(EPS)	Mr. B. Naresh	A grid tied SPV system with adaptive DC link voltage for CPI voltage variations using fuzzy logic control
B Naresh & M.Tech(EPS)	Mr. B. Naresh	To eradicate the harmonics at Micro grid by using dual interfacing Circuit
B. Srinivas & M.Tech(EPS)	Mrs. K. S Deva Rani	Increase the efficiency in wind turbine system by using DFIG
K. Rajesh & M.Tech(EPS)	Mr. B. Chandraiah	Design and Implementation of Multi-Input Transformer-Coupled Bidirectional DC-DC Converter with ANN Circuit
P. Rajesh & M.Tech(EPS)	Mrs. G. Swetha Bindu	High Effectiveness Wind Power Generation By Using DFIG With SSFCL
B. Bhargav Kumar & M.Tech(EPS)	Dr. N. Srinivasa Rao	Design and Implementation of high efficiency solar electric vehicle system by using SRM drives system
K. Arun Kumar Reddy & M.Tech(EPS)	Ms. P. Prasanna Kumari	Propose and accomplishment of soaring efficiency power by using brushless converter
K. Srikanth & M.Tech(EPS)	Mr. L. Phani Kumar	An Enhanced Power Sharing Scheme For Voltage Unbalance and Harmonic Compensation in an Islanded AC Micro grid.
R. Ramesh & M.Tech(EPS)	Mr. L. Phani Kumar	Practical Model On A Hybrid Series Active Power Compensator For Increase The Power Quality
A. Prajyusha & M.Tech(EPS)	Dr. N. Srinivasa Rao	Development Of Ultra-Capacitor Based DVR For Power Quality Improvement
V Chandra Kanth & M.Tech(EPS)	Dr. N. Srinivasa Rao	Development Of Circuit For Standalone Solar PV Hybrid System
G Bhavsingh & M.Tech(EPS)	Mr. B. Naresh	Energy endorsement system increased for analyze in effort and component benefit in micro grid

# Papers published / Presented by UG students:

## BEST PROJECTS FOR CAYm1 (2017-18)

ВАТСН	ROLL NUMBERS	NAME OF THE GUIDE	TITLE OF THE PROJECT	Publication/ Achievement
	14X01A0202		Photovoltaic module Integrated stand-	II Prize(Project expo in St. Peters Engineering College)
1	14X01A0204	Mr.B.Chandraiah	alone single stage switched capacity inverter with	
	14X01A0217		maximum power point tracking.	
	14X01A0209		An Induction generator based AC/DC Hybrid	Double in the Line
2	14X01A0213	Dr.Srinivasa Rao	Electric power generation system for more Electric Aircraft (MEA)	Participated in Project Expo in Malla Reddy Institute
	14X01A0214			
	15X05A0202		The Solar LED street Light	I prize (Project Expo in Malla Reddy Institute of
3	15X05A0206	Mr. Y.Narasimha Rao		Engineering and Technology)
	15X05A0209			
4	15X05A0203			Project expo in St. Peters Engineering College
	15X05A0205	Mr. B.Chandraiah	Flexible Power Electronic Transformer	
	15X05A0212			

# Best Projects for CAYm2 (2016-17)

BATC H	ROLL NUMBERS	NAME OF THE GUIDE	TITLE OF THE PROJECT	Publication/ Achievement
	13X01A0224		Five Level Inverter For	I prize (Project expo in St. Peters
1	13X01A0212			Engineering College)
	14X05A0204			

2	13X01A0220 13X01A0222 13X01A0217	Mr. P.chaitanya Deepak	A Cascaded Multilevel Inverter Based On Capacitor- Switched For High Frequency Of Ac Power Distribution System	Participated in Project Expo in Malla Reddy Institute of Engineering and Technology
	13X01A0213 14X05A0206			
3	13X01A0210	Mrs.K.S.Deva Rani	Two Stage Solar Photovoltaic Based Stand Alone Scheme Having Battery As Energy Storage Element For Rural Development	II Prize (Project expo in St. Martins Engineering College)
	13X01A0209			
	13X01A0227			
	14X05A0203			
	13X01A0219	Mr.J.Ravi Kiran	A Novel High Step-Up Dc/Dc Converter Based On Integrating Coupled Inductor And Switched-Capacitor Techniques For Renewable Energy Applications	Participated In Project expo in St. Martins Engineering College
4	13X01A0216			
	14X05A0205			
	13X01A0206			
	13X01A0223		Cascaded Two-Level Inverter- Based Multilevel STATCOM For High-Power Applications	Participated in Project Expo in
5	14X05A0208	Mr.Y.Narsimha Rao		Malla Reddy Institute of Engineering and
	13X01A0226			Technology