

UNIT-IV

ARM Architecture

1. ARM stands for

- a) Advanced RISC Machine
- b) Advanced Reduced Machine
- c) Automatic RISC Machine
- d) Automatic Reduced Machine

Answer: a) Advanced RISC Machine

2. ARM is based on

- a) CISC
- b) RISC
- c) VLIW
- d) DSP

Answer: b) RISC

3. CPSR stands for

- a) Current Program Status Register
- b) Current Processor Status Register
- c) Common Processor Status Register
- d) Common Program Status Register

Answer: b) Current Processor Status Register

4. ARM pipeline generally consists of

- a) 2 stages
- b) 3 stages
- c) 5 stages
- d) 8 stages

Answer: b) 3 stages

5. Thumb instructions are

- a) 8-bit
- b) 16-bit
- c) 24-bit
- d) 32-bit

Answer: b) 16-bit

Fill in the Blanks

1. ARM is a RISC architecture.
2. CPSR stands for Current Program Status Register (often expanded as Current Program/Processor Status Register depending on context).
3. ARM supports conditional execution.
4. Thumb instructions are 16-bit instructions.
5. ARM uses a pipeline to improve performance.
6. Software interrupts are generated using the SWI instruction.