

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH. (POWER ELECTRONICS) 2009-10
COURSE STRUCTURE AND SYLLABUS

I SEMESTER

Code	Group	Subject	L	P	Credit
		Machine Modeling & Analysis	3	0	3
		Analysis of Power Electronic Converters	3	0	3
		Modern Control Theory	3	0	3
		Power electronic Control of DC Drives	3	0	3
	Elective –I	HVDC Transmission Operations Research Modern Power electronics	3	0	3
	Elective -II	Programmable logic controllers and their applications Energy Conservation Systems Dynamics of Electrical Machines	3	0	3
	Lab	Power Converters Lab	0	3	2
		Seminar	-	-	2
		Total Credits (6 Theory + 1 Lab.)			22

II SEMESTER

Code	Group	Subject	L	P	Credit
		Power Electronic Control of AC Drives	3	0	3
		Microprocessor and Microcontroller	3	0	3
		Flexible AC Transmission Systems (FACTS)	3	0	3
		Neural And Fuzzy Systems	3	0	3
	Elective -III	Digital Control Systems Power Quality Advanced Digital Signal Processing	3	0	3
	Elective -IV	Reliability Engineering Enterprise Resource Planning Embedded Systems	3	0	3
	Lab	Electrical Systems Simulation Lab	0	3	2
		Seminar	-	-	2
		Total Credits (6 Theory + 1 Lab.)			22

II YEAR - I Semester

Code	Group	Subject	L	P	Credit
		Comprehensive Viva	-	-	2
		Project Seminar	0	3	2
		Project work	-	-	18
		Total Credits			22

II YEAR - II Semester

Code	Group	Subject	L	P	Credit
		Project work and Seminar	-	-	22